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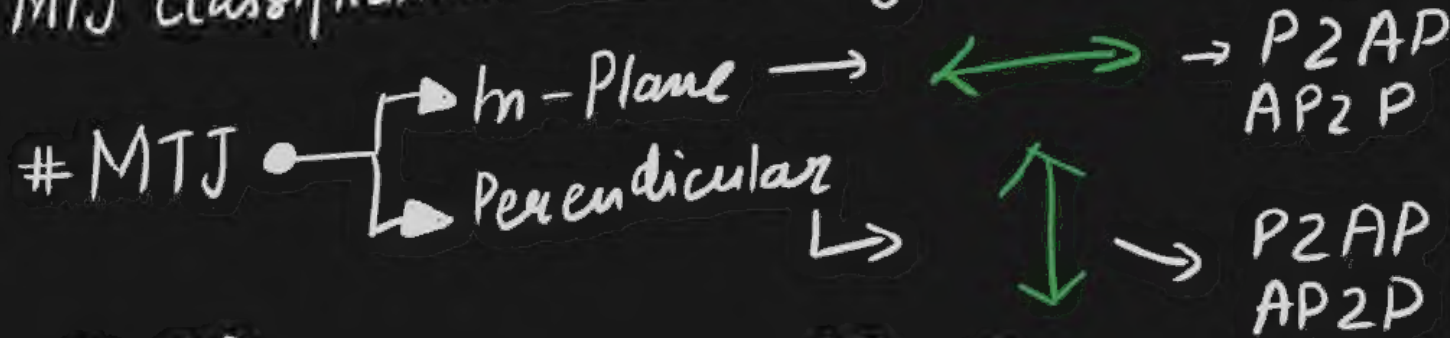
3 - Terminal MTJ



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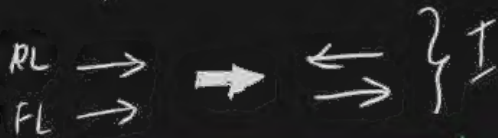
The IDEA

MTJ Classification based on Magnetic Anisotropy



P2AP

parallel to Antiparallel



more current needed

less desired

AP2P

Antiparallel to Parallel



less current needed

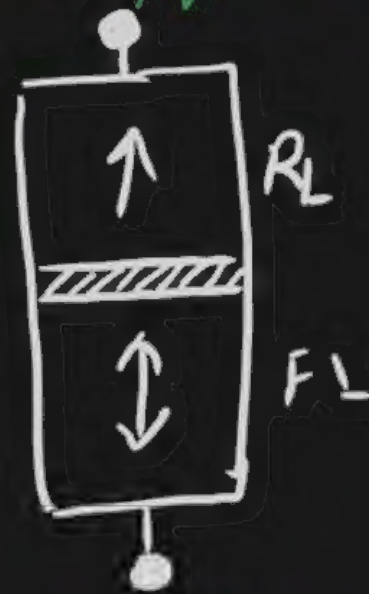
desirable condition

↑
integers
↑
S.

The **IDEA** is to combine the pMTJ (perpendicular) and the iMTJ (in-plane) to get a HYBRID MTJ that switches from perpendicular to in-plane so as to only operate in Anti-parallel to parallel configuration.

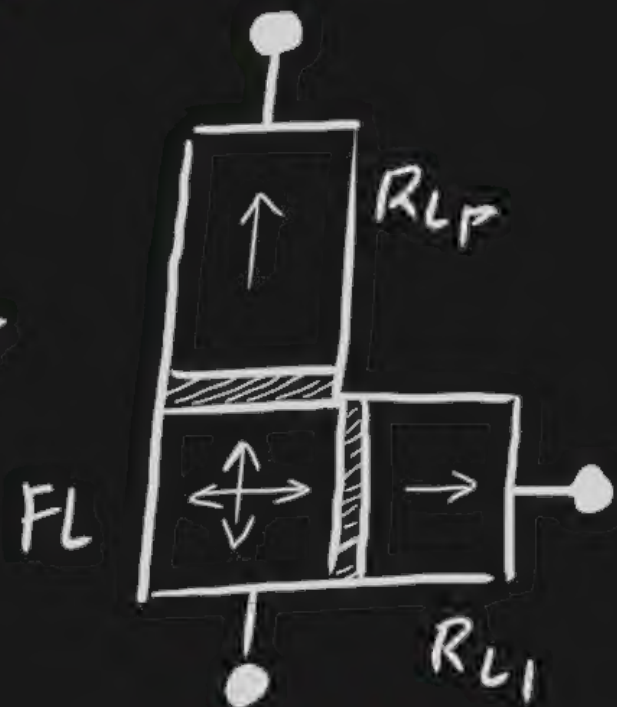


In-Plane MTJ



Perpendicular MTJ

=



Hybrid MTJ

Why?

Optimization of existing

CMOS - MTJ circuits

How?? → [1] Trying and always running
the MTJ in AP { Anti-parallel }
to P { Parallel } configuration.

↓
CURRENT DEMAND
drastically REDUCES

[2] Non-volatility
Better Processing speeds
Thermal stability

} general
advantages
of MTJs.

How

#

f

RL

FL

RL

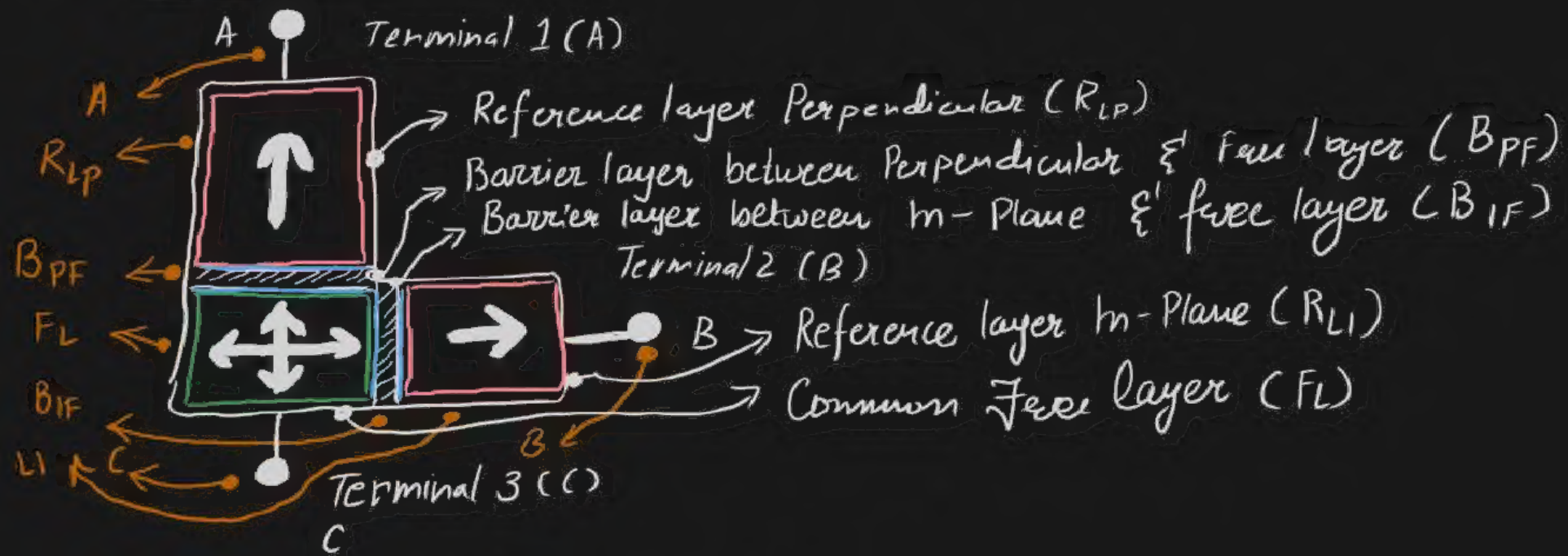
FL

HOW

Better Processing Speeds of MJTs
Thermal Stability

le

Proposed Structure :



- → Barrier layers → SEPARATE
- → Free layer → COMMON
- → Reference layers → SEPARATE

PROBLEMS:

- 1) LLGS equation only solves magnetization dynamics for either perpendicular OR in-plane.
 - ∴ incorporating them to work together on a hybrid device needs significant changes
- 2) let's ignore problem 1 & assume the proposed MTJ somehow works. Even if it switches states from AP to P with respect to pMTJ, in the immediate next switching cycle, the FL state in perpendicular configuration has to be modeled to be Anti-parallel with respect to iMTJ.
- 3) corresponding read and write drivers / circuits.
- 4) Usage & implementation of this in pre-existing circuits.

Potential Solutions

1) for problem 1,
lets say we divide LLG equation

$\frac{1}{2} \frac{d^2 \psi}{dx^2} = -\psi$ LLG

1)

for problem 1,
lets say we divide LLG equation into 2 components

$$LLG_{\text{overall}} = LLG_{\text{perpendicular}} + LLG_{\text{in-plane}}$$

we can introduce a multiplication factor here and call it the "type" parameter. type = 1 \rightarrow P type = 2 \rightarrow I

Whenever p [perpendicular] is ON we want the i [in-plane] part to be off, and vice versa.

So, we can use MA in the equation above to selectively activate components. $\rightarrow MA = \text{type} \leftarrow \text{same values}$

$$LLG_{\text{overall}} = \{ [MA] [LLG_p] \} + \{ [1-MA] \times [LLG_i] \} \rightarrow \textcircled{1}$$

now, let's say we want the LLG to be solved for the instant where it operates in perpendicular configuration then $\text{type} = 1$,

Substitute in eqⁿ (1):

$$\text{LLG}_{\text{overall}} = \overset{\text{finite}}{\cancel{(1 \times \text{LLG}_p)}} + \overset{0}{\cancel{((1-1) \times \text{LLG}_i)}}$$

$$\therefore \text{LLG}_{\text{overall}} = \text{LLG}_p$$

Similarly, if it is working in in-plane config:

$$\text{type} = 0,$$

$$\text{LLG}_{\text{overall}} = \overset{0}{\cancel{(0 \times \text{LLG}_p)}} + \overset{\text{finite}}{\cancel{(1-0) \times \text{LLG}_i}}$$

$$\therefore \text{LLG}_{\text{overall}} = \text{LLG}_i$$

2] 2.1) investigation into easy axis based switching
↳ ongoing

2.2) mapping P states to I states
by developing some algorithm

Problem 3 & 4



Ideal to look into
post-model development as
they are circuital implementation
concerns.

Thank you!